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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **WATANABE, Kenichi**

Group Art Unit: **2815**

Serial No.: **10/050,171**

Examiner: **WARREN, Matthew E.**

Filed: **January 18, 2002**

P.T.O. Confirmation No.: 7623

**FOR: SEMICONDUCTOR DEVICE CAPABLE OF SUPPRESSING CURRENT
CONCENTRATION IN PAD AND ITS MANUFACTURE METHOD**

REQUEST FOR STATUS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: May 11, 2006

Sir:


The undersigned attorney requests the status of the above-identified application.

A response to the official Action of June 29, 2005 was filed on **August 30, 2005** in the
U.S. Patent and Trademark Office and to date no further Office Action has been received.

In the event any fees are required in connection with this paper, please charge Deposit
Account No. 01-2340.

Respectfully submitted,

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